

# University of Mumbai



## 1T01116

FIRST HALF of (Summer) 2019

**EXAMINATION TIME TABLE**  
**PROGRAMME - T.E. (Electronics ) (REV. -2012)(CBSGS)**  
**SEMESTER - VI**

Days and Dates	Time	Paper Code	Paper
Friday, May 10, 2019	02:30 p.m. to 05:30 p.m.	37201	BASIC VLSI DESIGN
Thursday, May 16, 2019	02:30 p.m. to 05:30 p.m.	37202	ADVANCED INSTRUMENTATION SYSTEM
Wednesday, May 22, 2019	02:30 p.m. to 05:30 p.m.	37203	COMPUTER ORGANIZATION
Tuesday, May 28, 2019	02:30 p.m. to 05:30 p.m.	37204	POWER ELECTRONICS - I
Monday, June 03, 2019	02:30 p.m. to 05:30 p.m.	37205	DIGITAL SIGNAL PROCESSING AND PROCESSORS
Monday, June 10, 2019	02:30 p.m. to 04:30 p.m.	37206	MODERN INFORMATION TECHNOLOGY FOR MANAGEMENT

**Important Note:** •The candidates appearing for the examination should report 20 minutes before the start of examination.

- Mobile phones and other electronic gazets are prohibited in the examination hall.
- Change if any, in the time table shall be communicated on the university web site.

Mumbai - 400 098  
14th March, 2019.

**Dr. Vinod P. Patil**  
Director  
Board of Examinations & Evaluation

ST-271